**IN THE CLAIMS:**

Please cancel claims 1 and 7-11 without prejudice or disclaimer, amend claims 2-6, and add a new claim 12 as follows:

1. (Canceled)

2. (Currently Amended) An imaging system including a solid-state CMOS imaging device and a signal processing semiconductor integrated circuit for processing read-out signals of pixels from said solid-state CMOS imaging device, comprising:

first level detection means which detects ~~for detecting~~ brightness on a first area set up on an imaging area of said solid-state CMOS imaging device, the first area which is predetermined area in a frame;

second level detection means which detects ~~for detecting~~ brightness on a second area which is set up on an imaging area of said solid-state CMOS imaging device, and is larger than said first area, the second area which is a predetermined area in the frame; and

judgment means which judges turning-on-and-off of a light source illuminating in accordance with an object to be imaged on the basis of detection levels of said first and second level detection means.

3. (Currently Amended) An imaging system according to Claim 2, wherein said judgment means that said light source illuminating in accordance with the object is turned on and off when variation in the detection level of said first level detection means is ~~large~~ larger than a predetermined level and variation in the detection level of said second level detection means is ~~small~~ smaller than a second predetermined level.

4. (Currently Amended) An imaging system according to Claim 2, wherein said first area is constituted by pixels on a single horizontal scanning line and said second area is constituted by pixels on a plurality of horizontal scanning lines,

5. (Currently Amended) An imaging system according to Claim 2, further comprising charge storage control means which sets up ~~for setting~~ an electric charge storage time for each pixel of said solid-state CMOS imaging device to be equal to a turning-on-

and-off period of said light source illuminating in accordance with the object or an integral multiple thereof to thereby remove flicker.

6. (Currently Amended) An imaging system including a solid-state CMOS imaging device and a signal processing semiconductor integrated circuit for processing read-out signals of pixels from said solid-state CMOS imaging device ~~according to Claim 2~~, comprising:

first level detection means for detecting brightness on a first area set up on an imaging area of said solid-state CMOS imaging device;

second level detection means for detecting brightness on a second area larger than said first area;

judgment means for judging turning-on-and-off of a light source illuminating an object to be imaged on the basis of detection levels of said first and second level detection means; and

a control unit for setting up an electric charge storage time for each pixel of said solid-state CMOS imaging device by means of processing in accordance with a program, [[and]] wherein said judgment of the turning-on-and-off of said light source illuminating in accordance with the object on the basis of the detection levels of said first and second level detection means is performed by processing in accordance with [[a]] the program in said control unit,

7-11. (Canceled)

12. (New) An imaging system according to Claim 2, further comprising:

a control unit for setting up an electric charge storage time for each pixel of said solid-state CMOS imaging device by means of processing in accordance with a program,

wherein said judgment of the turning-on-and-off of said light source illuminating in accordance with the object on the basis of the detection levels of said first and second level detection means is performed by processing in accordance with the program in said control unit.